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— RCE  
2188

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Toshikazu NAKAMURA

Atty. Docket No. 108066-00038

Application No: 09/922,742

Examiner: P. Vital

Filed: August 7, 2001

Art Unit: 2188

For: CLOCK SYNCHRONIZED DYNAMIC MEMORY AND CLOCK  
SYNCHRONIZED INTEGRATED CIRCUIT

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 10, 2004

Sir:

Prior to initial examination of the application, please amend the above-  
identified application as follows:

**RECEIVED**  
AUG 12 2004  
Technology Center 2100

08/11/2004 KBETEM1 00000033 09922742

01 FC:1202  
02 FC:1201

108.00 OP  
258.00 OP